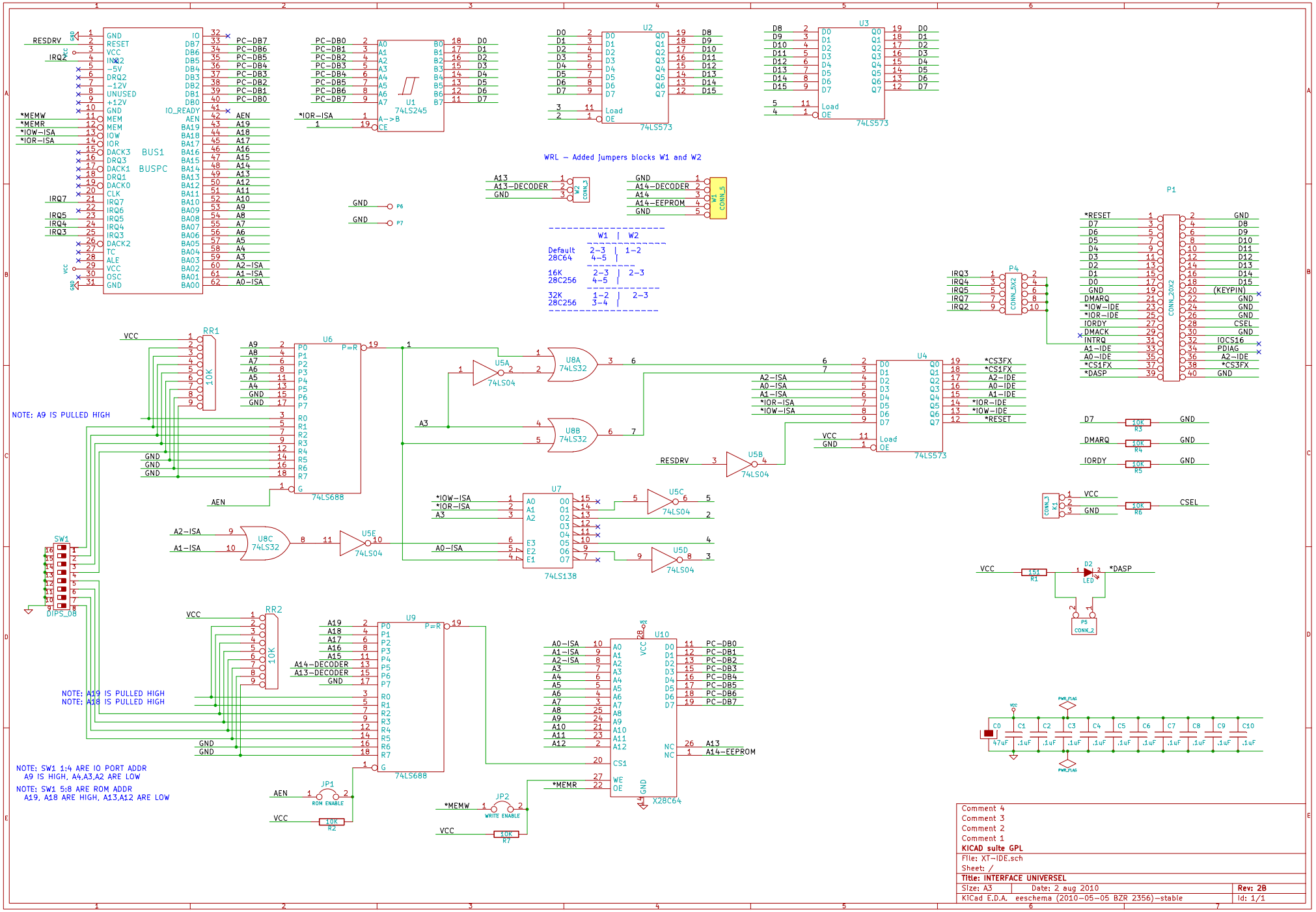
BUS1
BUSPC
U1 74LS245
U2 74LS573
U3 74LS573
WRL – Added jumpers blocks W1 and W2
W1 | W2
Default 28C64 2-3 | 1-2
4-5
16K 28C256 2-3 | 2-3
4-5
32K 28C256 1-2 | 2-3
3-4
P1
CONN_20X2
P4
CONN_5X2
RR1
10K
U6 74LS688
U5A 74LS04
U8A 74LS32
U8B 74LS32
U4 74LS573
U5B 74LS04
NOTE: A9 IS PULLED HIGH
U7 74LS138
U5C 74LS04
U5D 74LS04
U8C 74LS32
U5E 74LS04
SW1
DIPS_08
RR2
U9 74LS688
NOTE: A19 IS PULLED HIGH
NOTE: A18 IS PULLED HIGH
NOTE: SW1 1:4 ARE IO PORT ADDR
A9 IS HIGH, A4,A3,A2 ARE LOW
NOTE: SW1 5:8 ARE ROM ADDR
A19, A18 ARE HIGH, A13,A12 ARE LOW
JP1 ROM ENABLE
JP2 WRITE ENABLE
U10 X28C64
D2 LED
R1 151
P5 CONN_2
C0 47uF
C1 .1uF
C2 .1uF
C3 .1uF
C4 .1uF
C5 .1uF
C6 .1uF
C7 .1uF
C8 .1uF
C9 .1uF
C10 .1uF
PWR_FLAG
Comment 4
Comment 3
Comment 2
Comment 1
KICAD suite GPL
File: XT-IDE.sch
Sheet: /
Title: INTERFACE UNIVERSEL
Size: A3
Date: 2 aug 2010
Rev: 2B
KiCad E.D.A. eeschema (2010-05-05 BZR 2356)-stable
Id: 1/1